

Appl. No.: 09/448,175
Amdt. dated: 10/22/2004
Reply to Office Action of June 22, 2004

REMARKS/ARGUMENTS

Upon entry of the instant Amendment, claims 1-5 and 7-9 are pending. Claims 1-4, 8, and 9 have been amended to more particularly point out the applicant's invention. It is respectfully submitted that upon entry of the instant Amendment and consideration of the remarks below, the application is in condition for allowance.

CLAIM OBJECTIONS

A number of claim objections have been delineated in paragraph 1 of the detailed action. Except as noted below, each of these claims has been amended as suggested in paragraph 1. Claims 8 and 9 relate to a frequency synthesizer or combiner and not an analyzer. Thus, claims 8 and 9 have been amended to recite a frequency combiner. Accordingly, these objections should be obviated.

CLAIM REJECTIONS – 35 U.S.C. § 112

Claims 1-5 and 7-9 have been rejected under 35 U.S.C. § 112, first paragraph for failing to comply with the enabling requirement. In support of that rejection, paragraph 3 states that claims 1 and 8 recite a frequency analyzer configured to synthesize a plurality of input signals. Paragraph 3 also states that there is no description of frequency analyzer producing a single output signal.

It is respectfully submitted that claims 1-3 relate to a frequency analyzer for analyzing a plurality of input signals which also includes a plurality of output modulators. The recitation in claims 1-3 regarding a single output signal has been deleted. Accordingly, the rejection under 35 U.S.C. § 112 in this regard should be obviated. Claims 4 and 5 relate to a polyphase filter. These claims do not recite a single output signal. Accordingly, the rejection of these claims should be obviated.

Claims 8 and 9 relate to a frequency synthesizer or combiner. In order to more fully comply with the suggestions in paragraph 1 of the Detailed Action, frequency synthesizer was

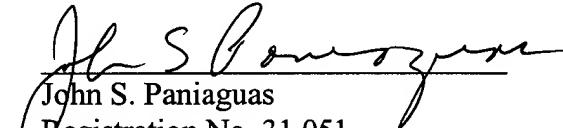
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changed to a frequency combiner. It is respectfully submitted that a frequency combiner/synthesizer is supported by the specification. For example, the Examiner's attention is directed to page 4, lines 25-27 ("the present invention relates to a discrete fourier transform DFT filter bank with stack shift capability, configured, for example, as an analyzer, and ***but equally applicable to a synthesizer ...***") The differences between a frequency synthesizer and an analyzer are illustrated in Figs. 1A and 1B. Accordingly, for all the above reasons, the Examiner is respectfully requested to reconsider and withdraw the rejections.

Respectfully submitted,

KATTEN MUCHIN ZAVIS ROSENMAN

By:



John S. Paniaguas
Registration No. 31,051
Attorney for Applicant(s)

Katten Muchin Zavis Rosenman
525 W. Monroe Street
Suite 1600
Chicago, Illinois 60661-3693
(312) 902-5312
(312) 577-4532
Customer No.: 27160